

FIG. 1

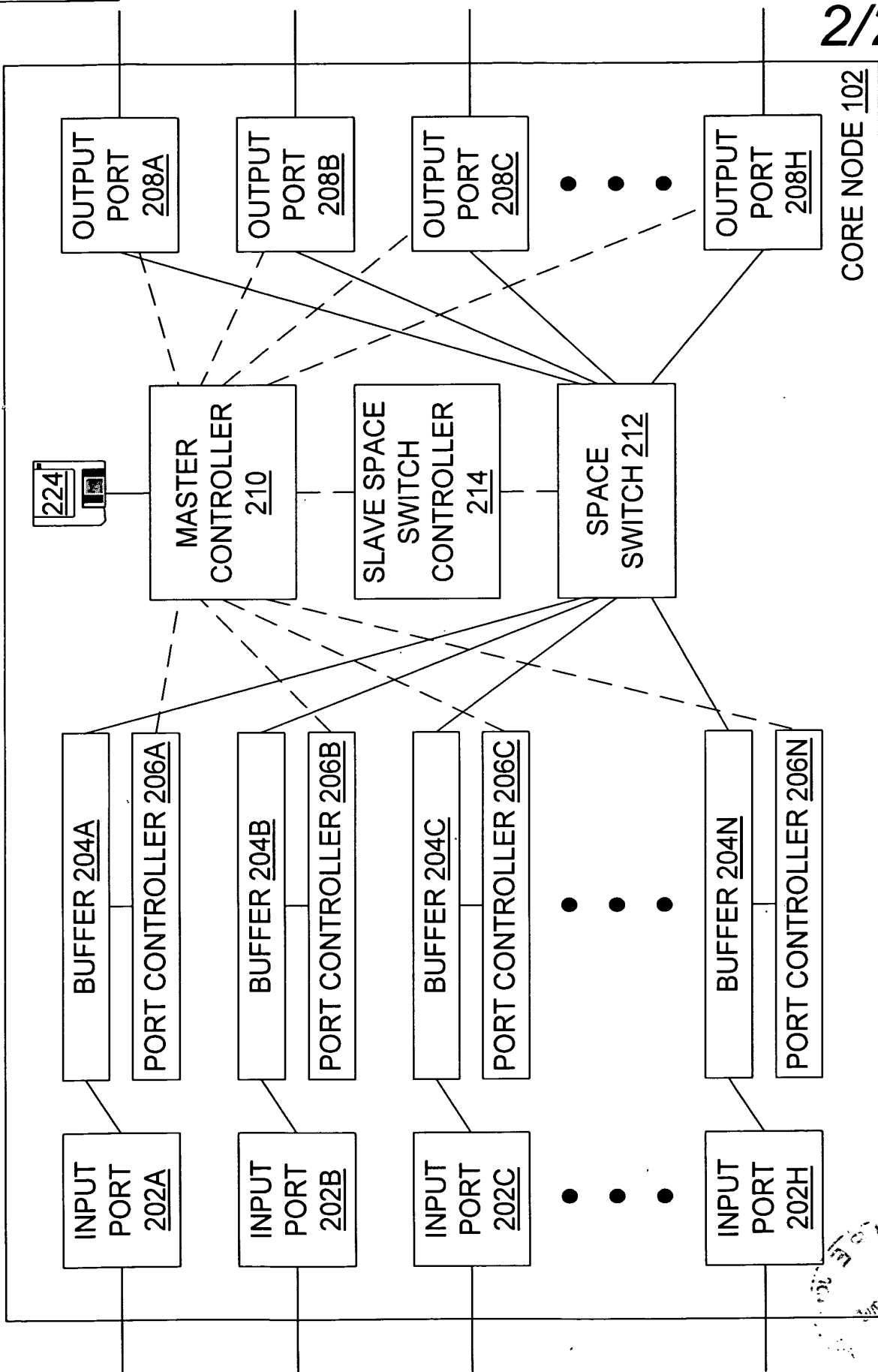
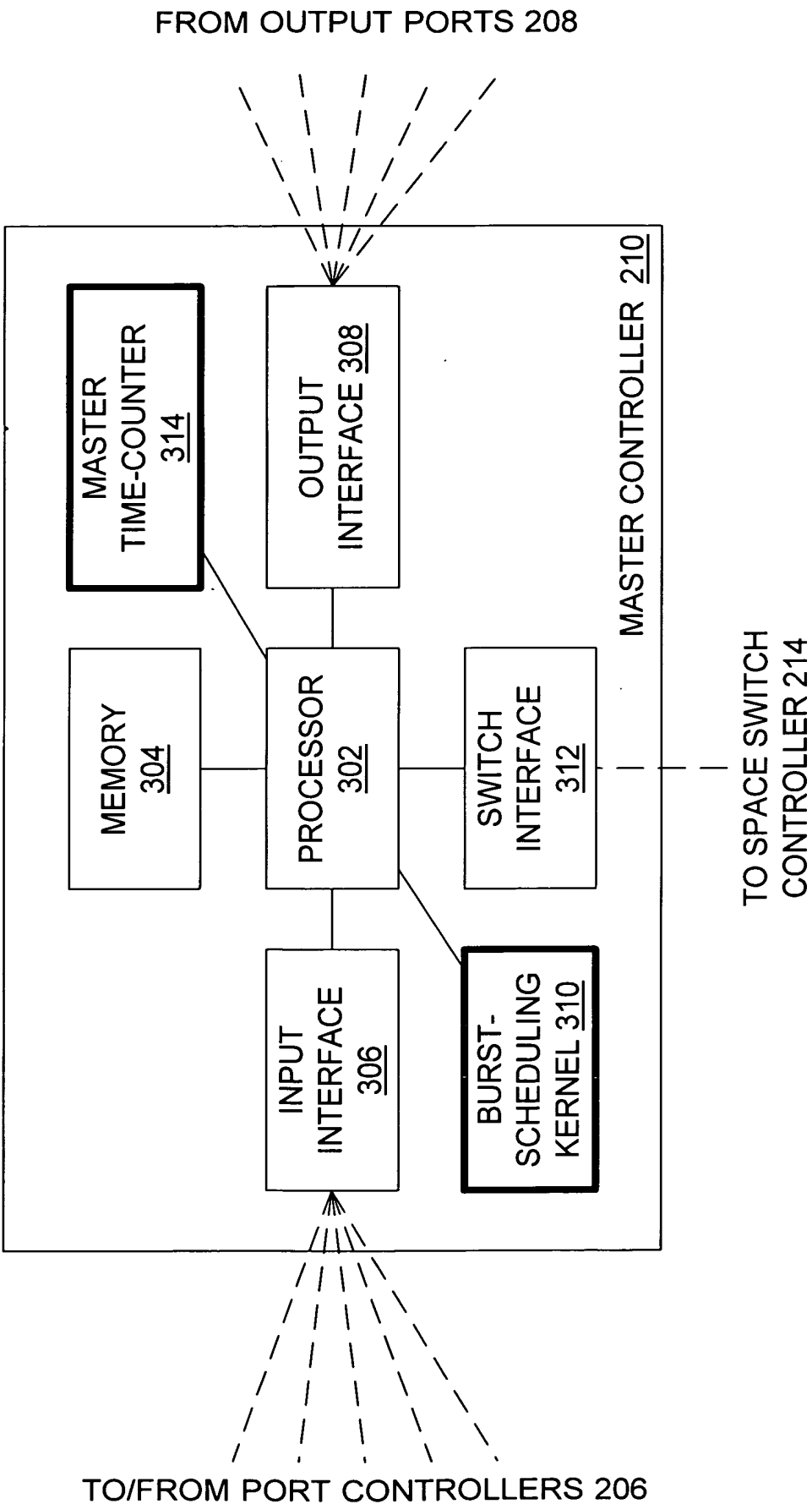
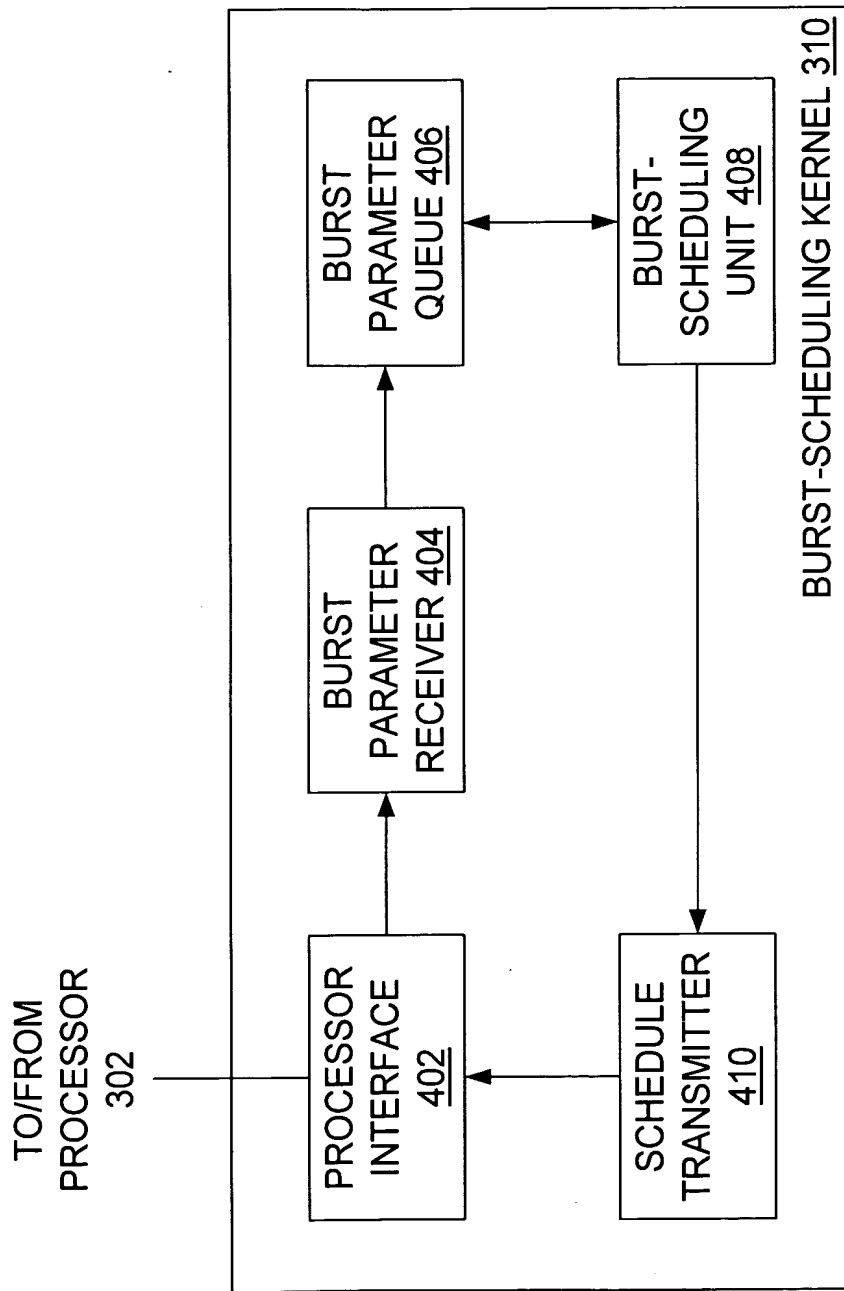


FIG. 2



**FIG. 4**

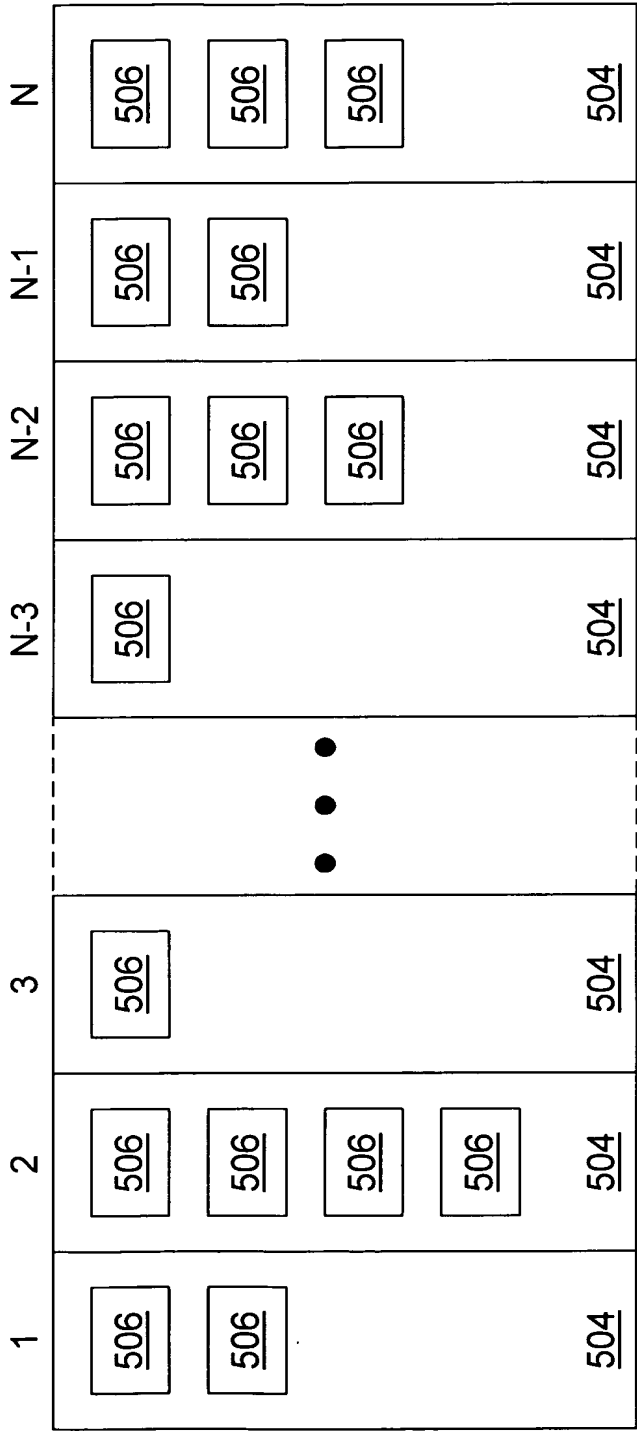


FIG. 5A

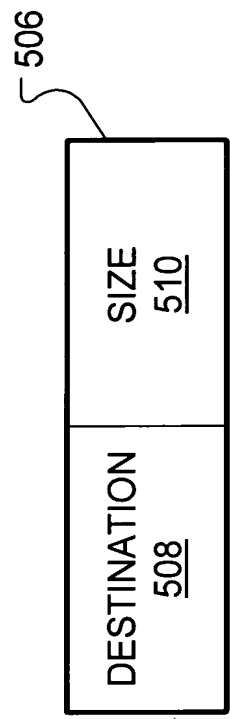
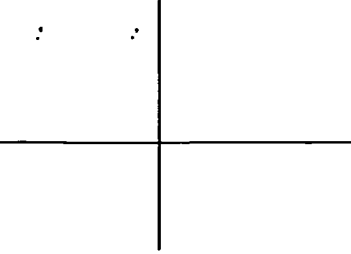


FIG. 5B

1	2	3	K-3	K-2	K-1	K
<div>NULL <u>608</u></div>	<div>ID <u>606</u></div>	<div>NULL <u>608</u></div>	<div>NULL <u>608</u></div>	<div>ID <u>606</u></div>	<div>NULL <u>608</u></div>	<div>NULL <u>606</u></div>
<u>604</u>	<u>604</u>	<u>604</u>	<u>604</u>	<u>604A</u>	<u>604</u>	<u>604</u>

600

FIG. 6



1	2	3	M-3	M-2	M-1	M
<u>706</u>	<u>706</u>	<u>706</u>	<u>706</u>	<u>706</u>	<u>706</u>	<u>706</u>
<u>704</u>	<u>704</u>	<u>704</u>	<u>704</u>	<u>704</u>	<u>704</u>	<u>704</u>

700 ↗



FIG. 7

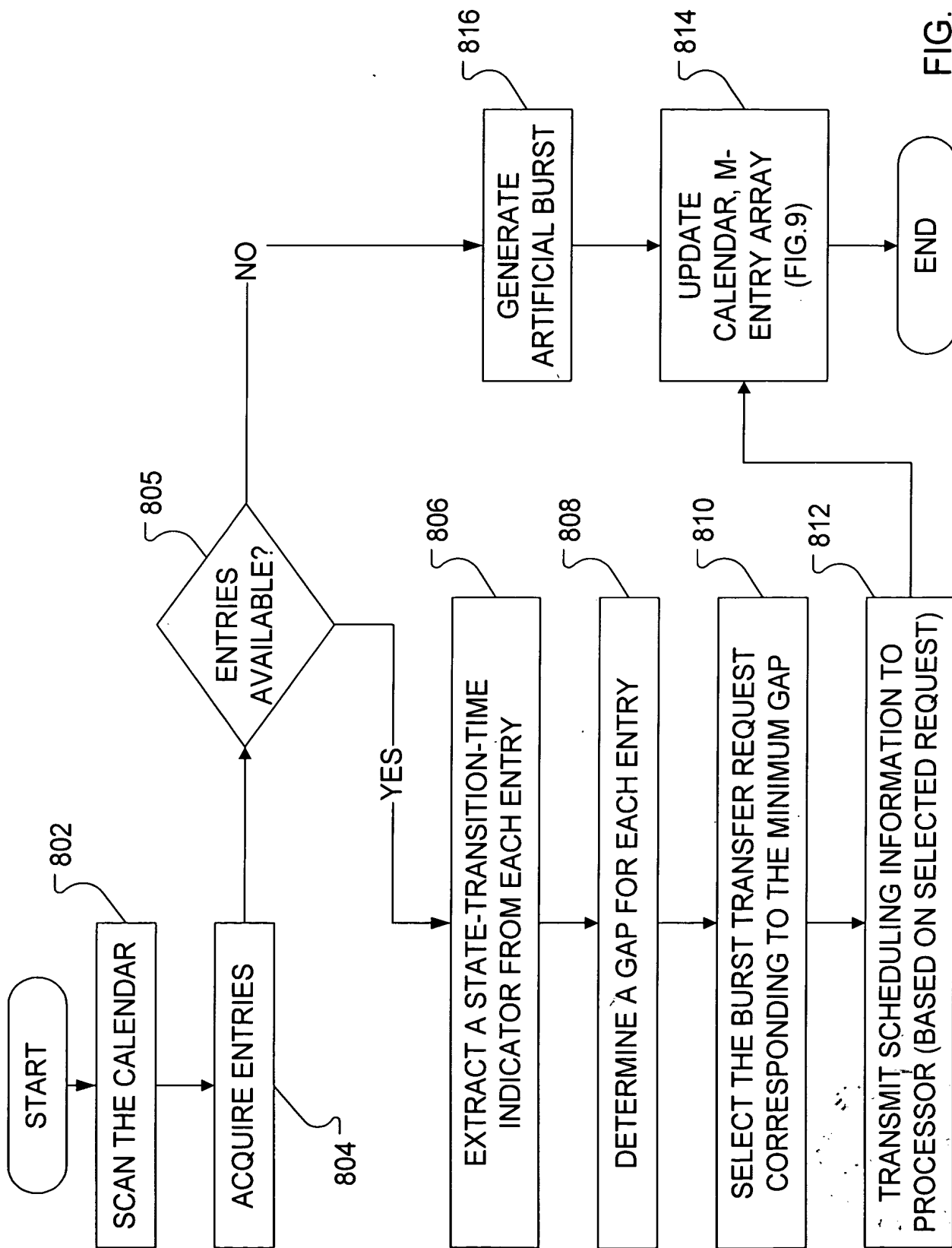
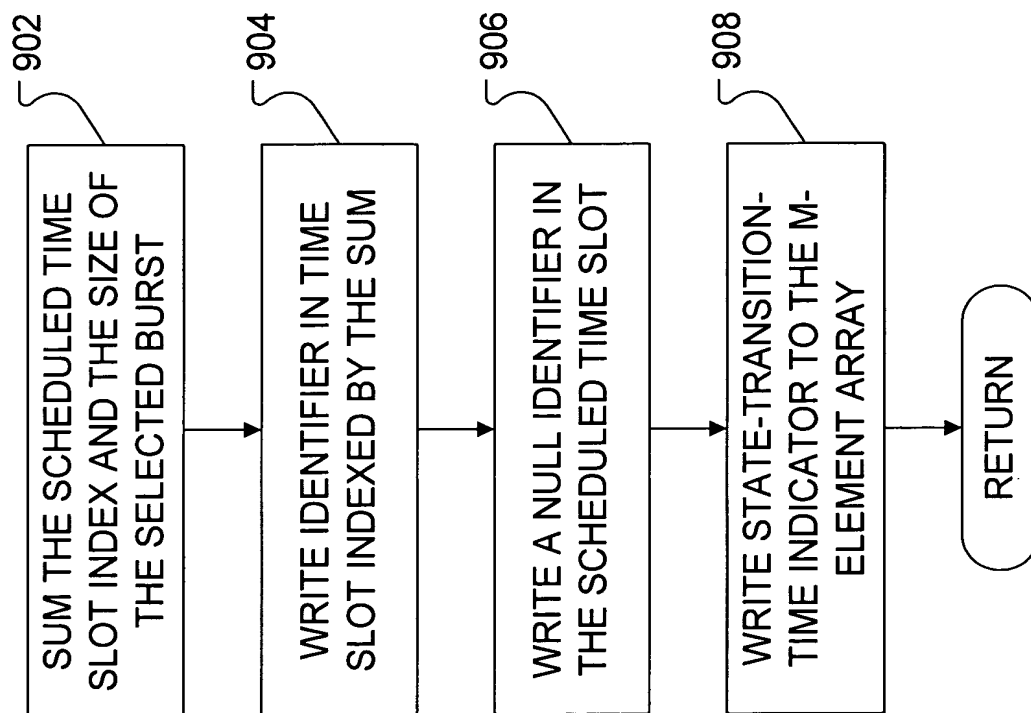


FIG. 8





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FIG. 9

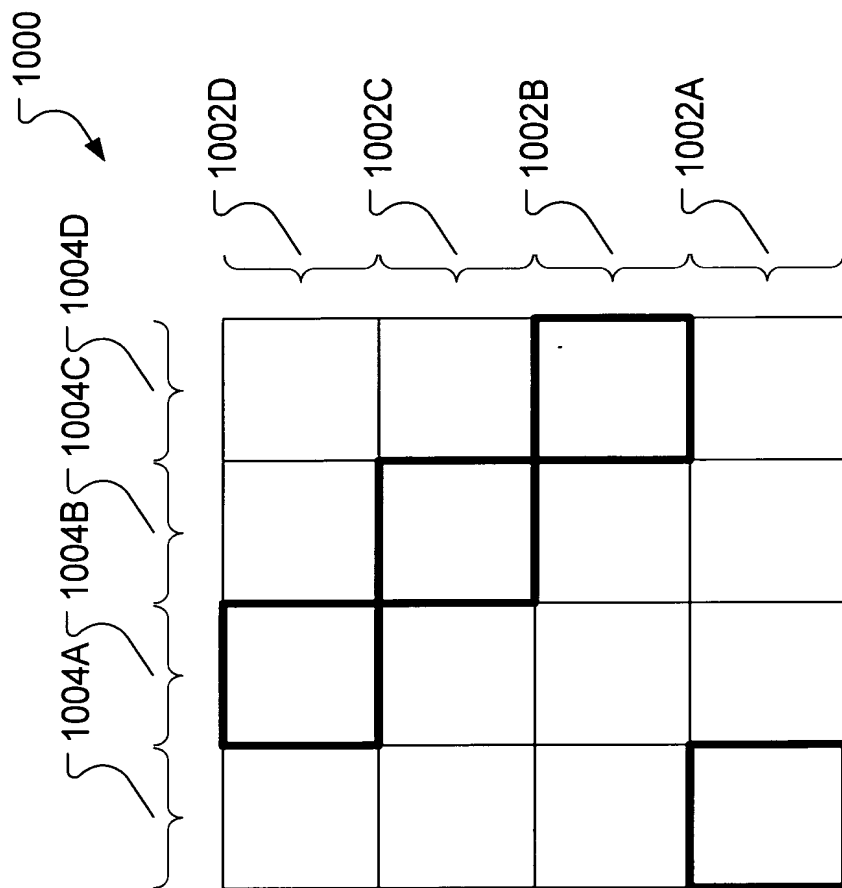


FIG. 10

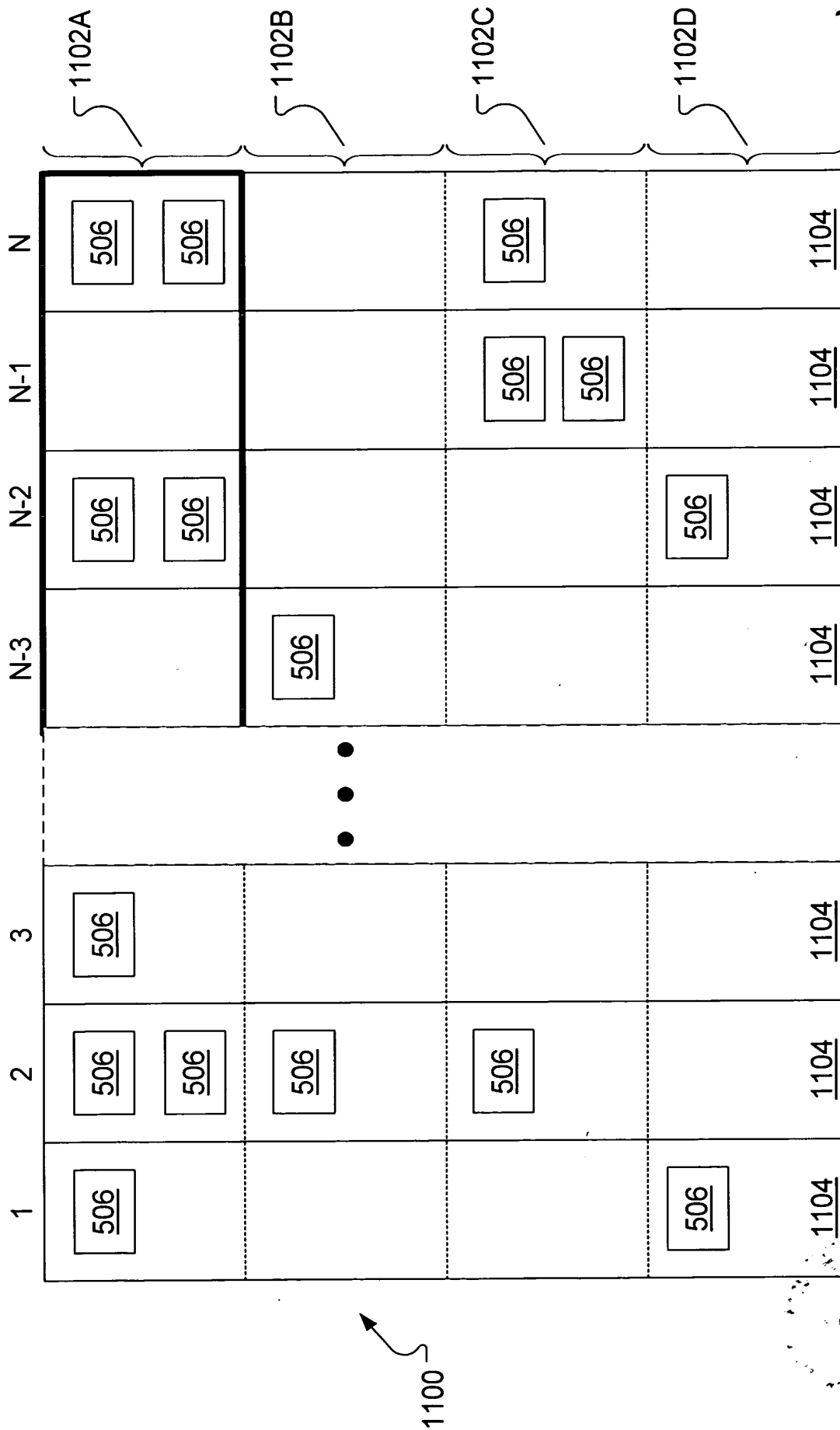
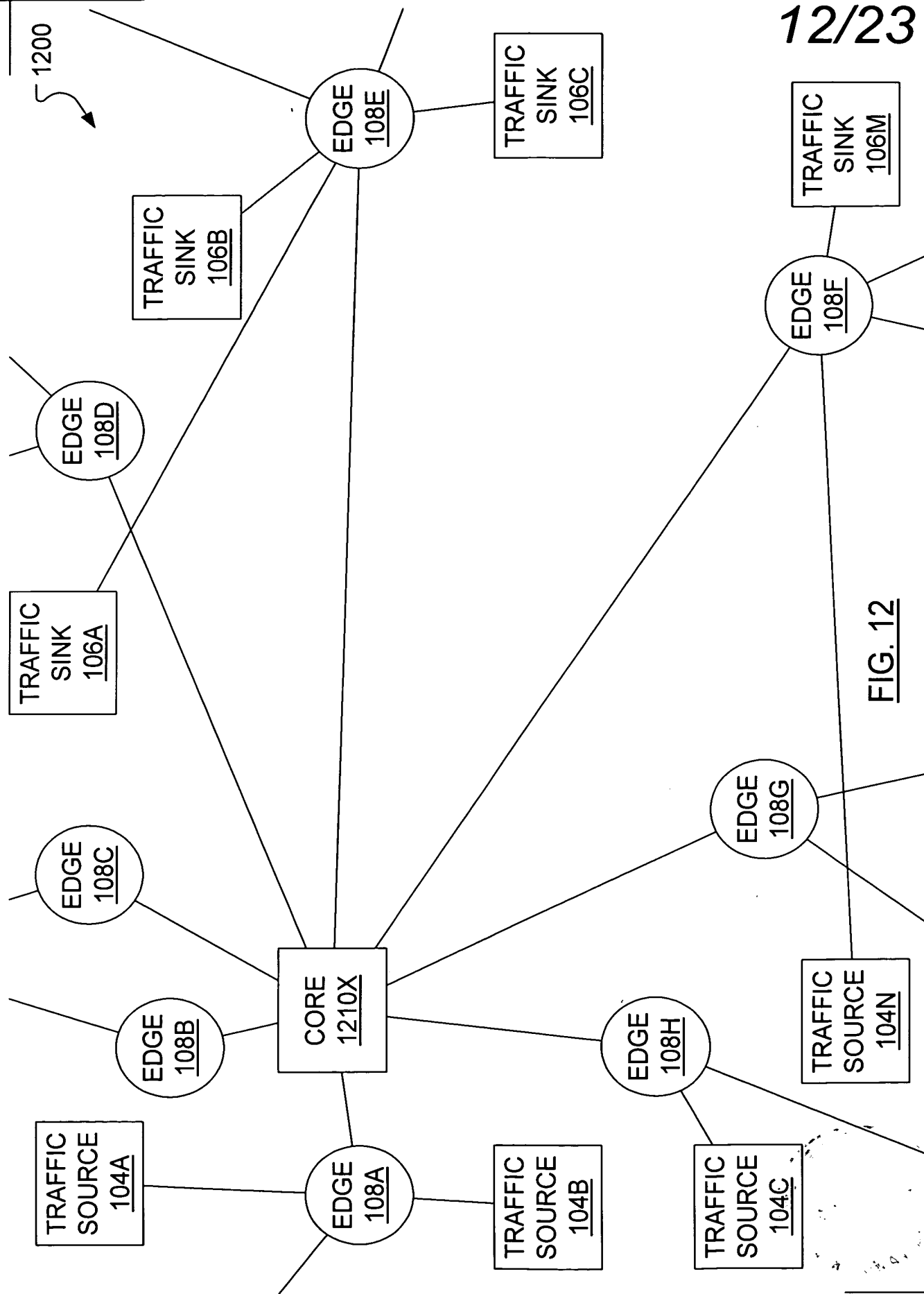


FIG. 11



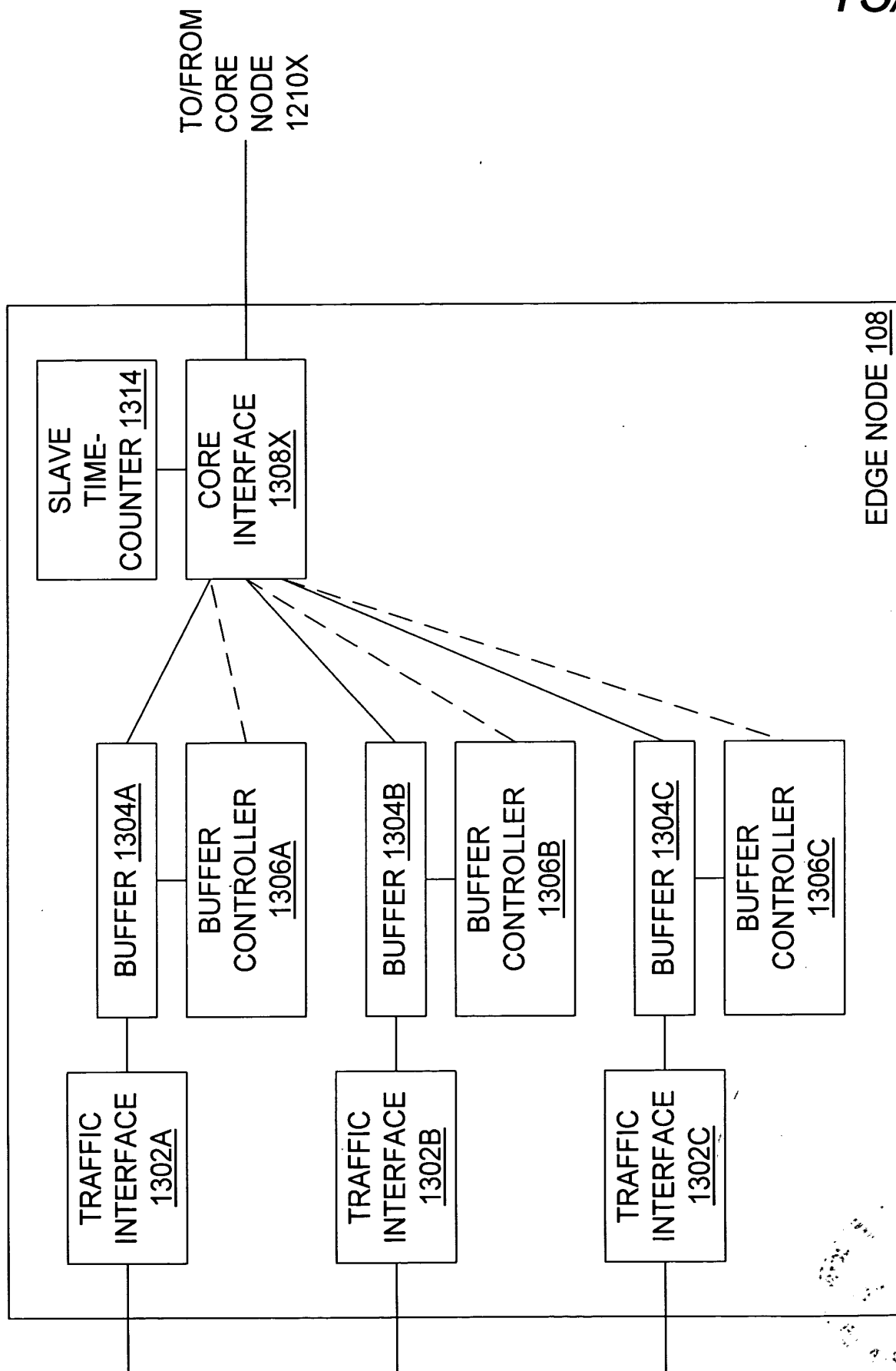
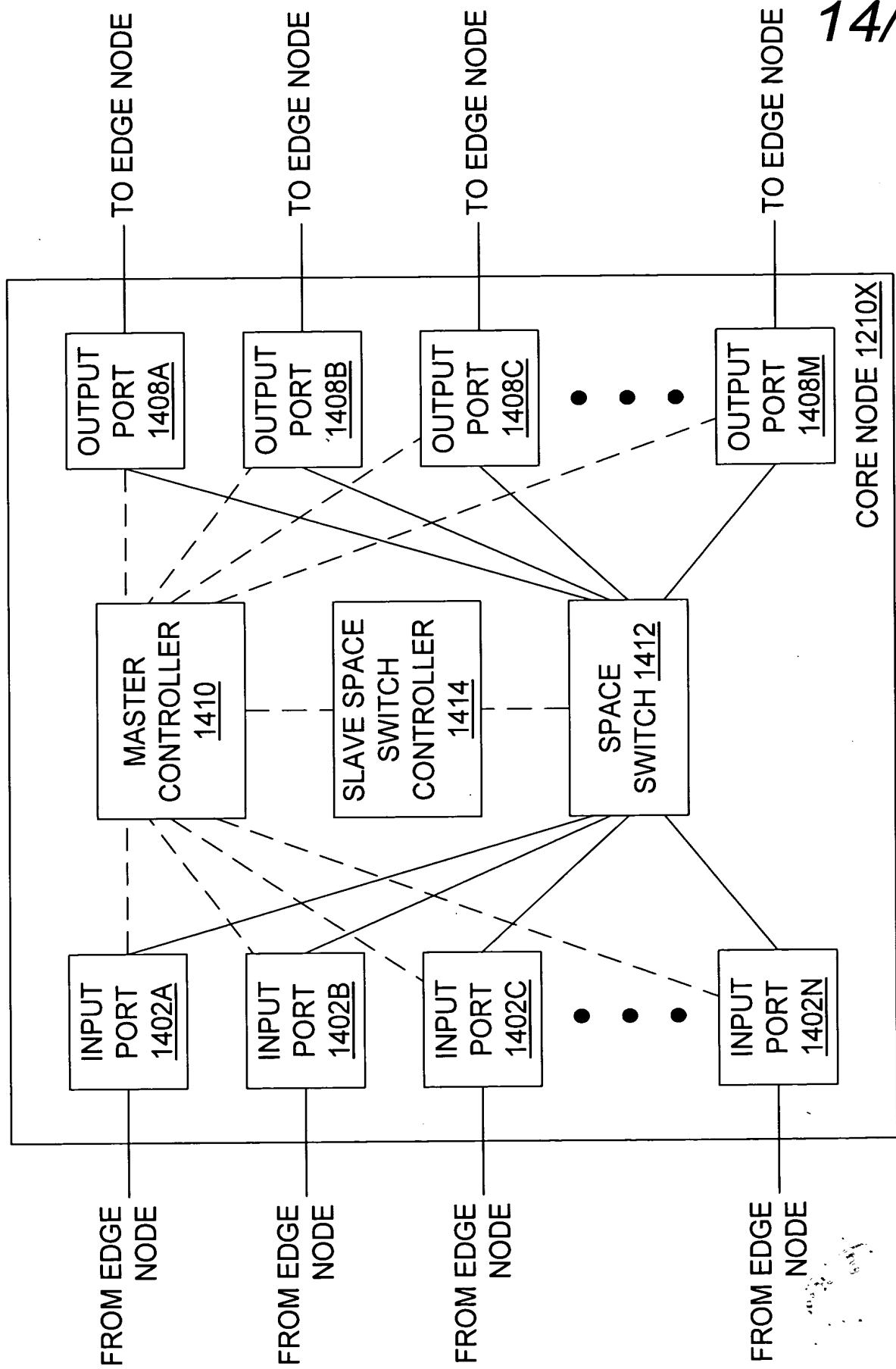


FIG. 13



CORE NODE 1210X

FIG. 14

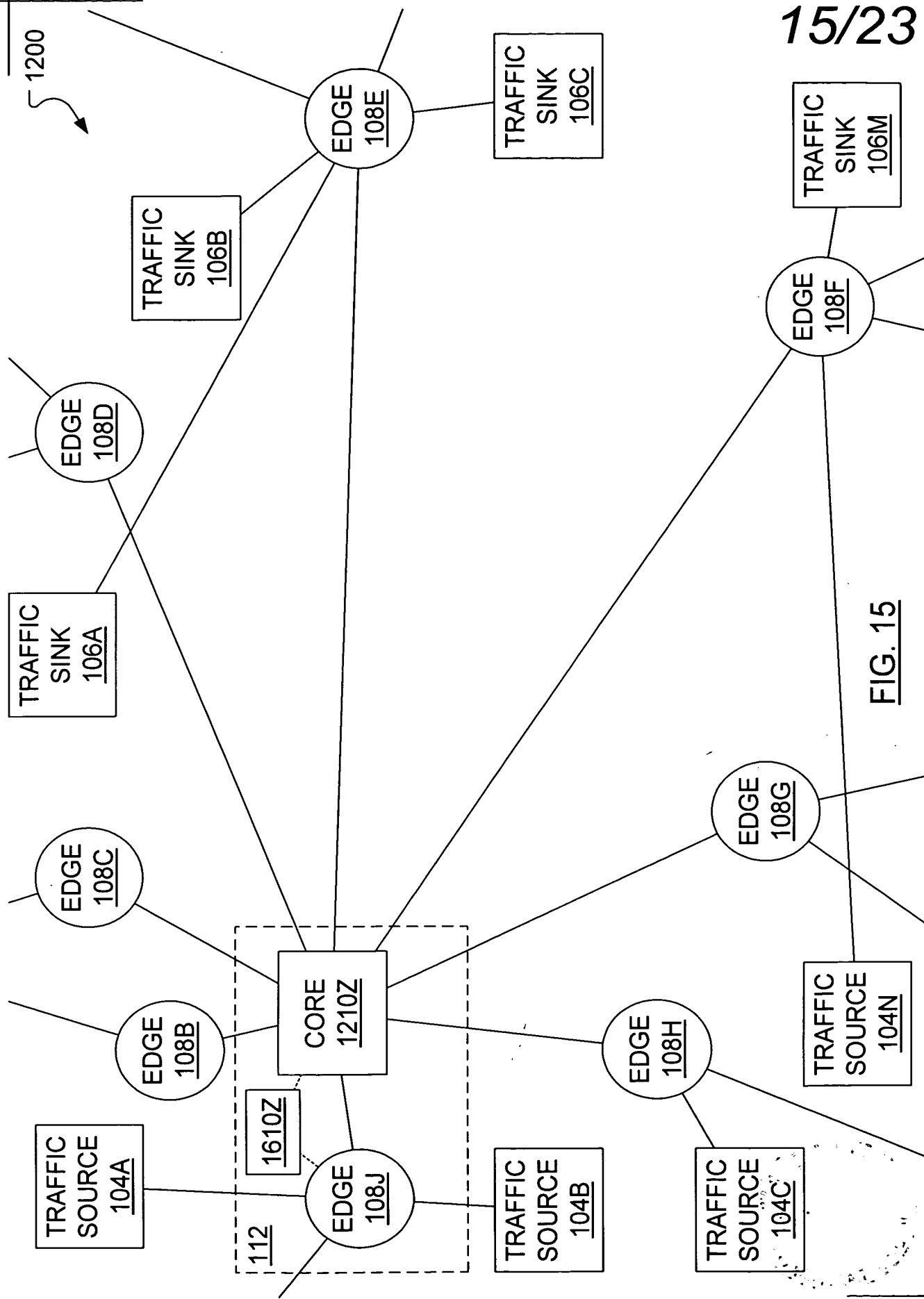


FIG. 15

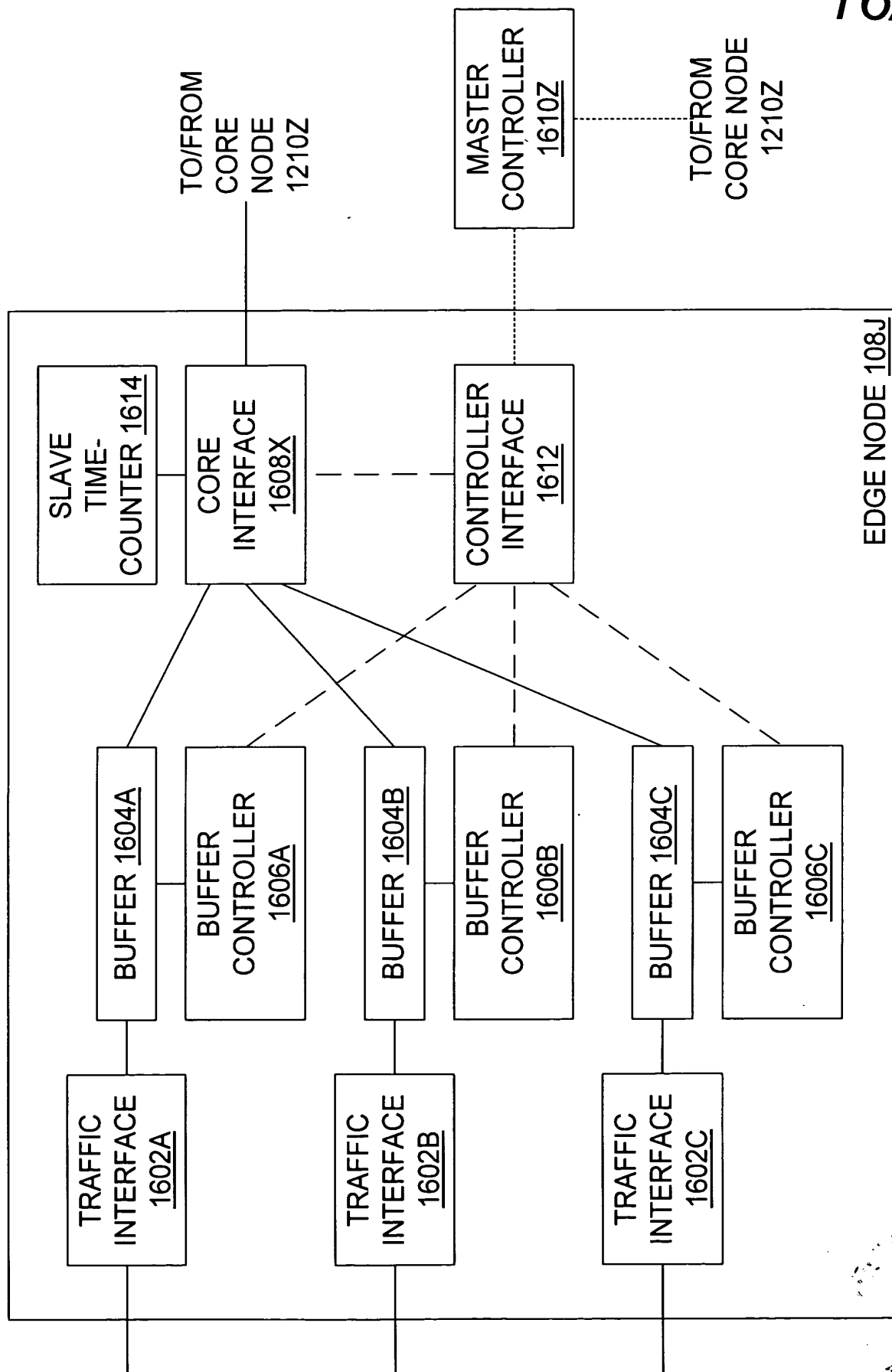


FIG. 16



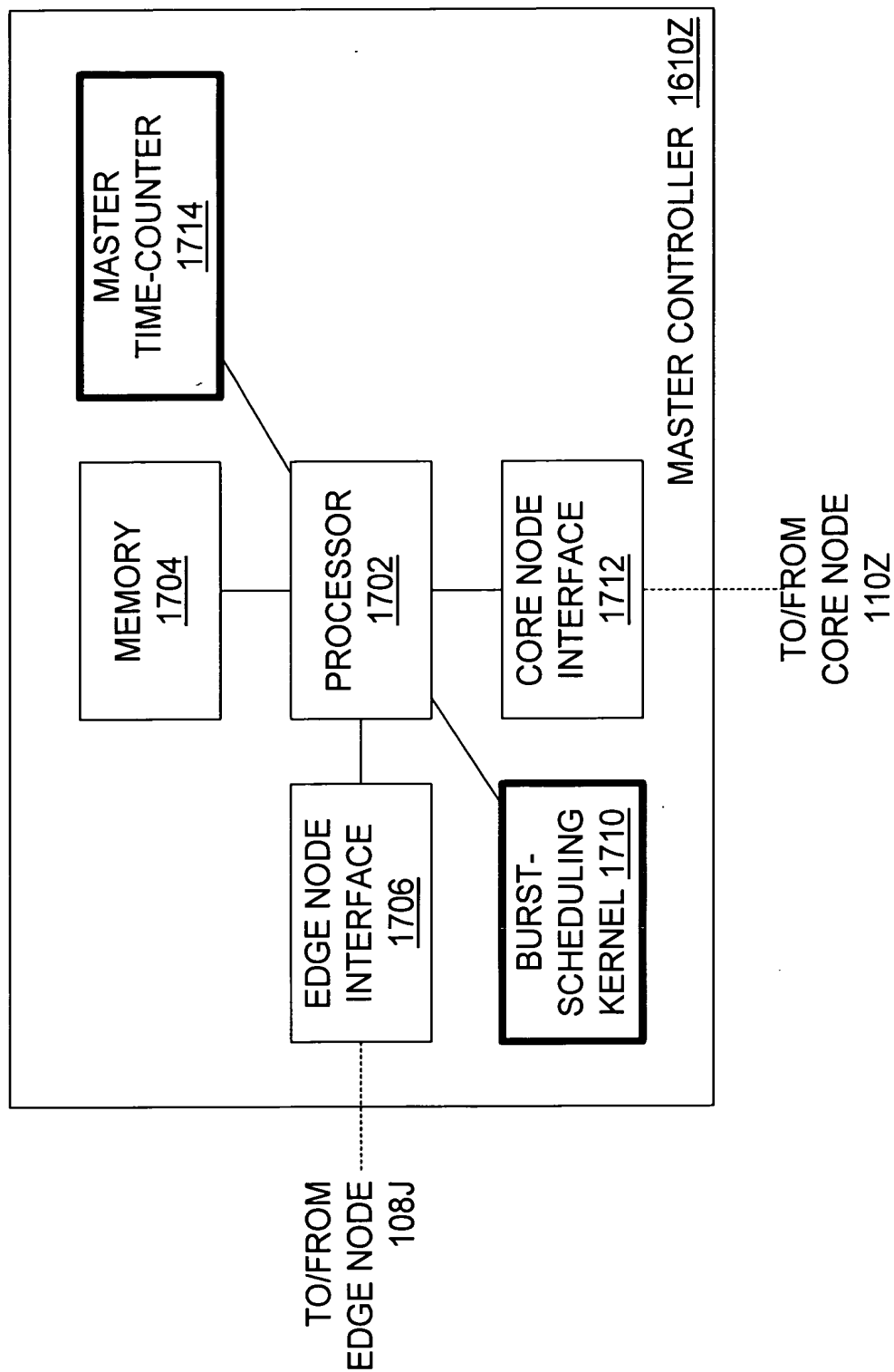


FIG. 17

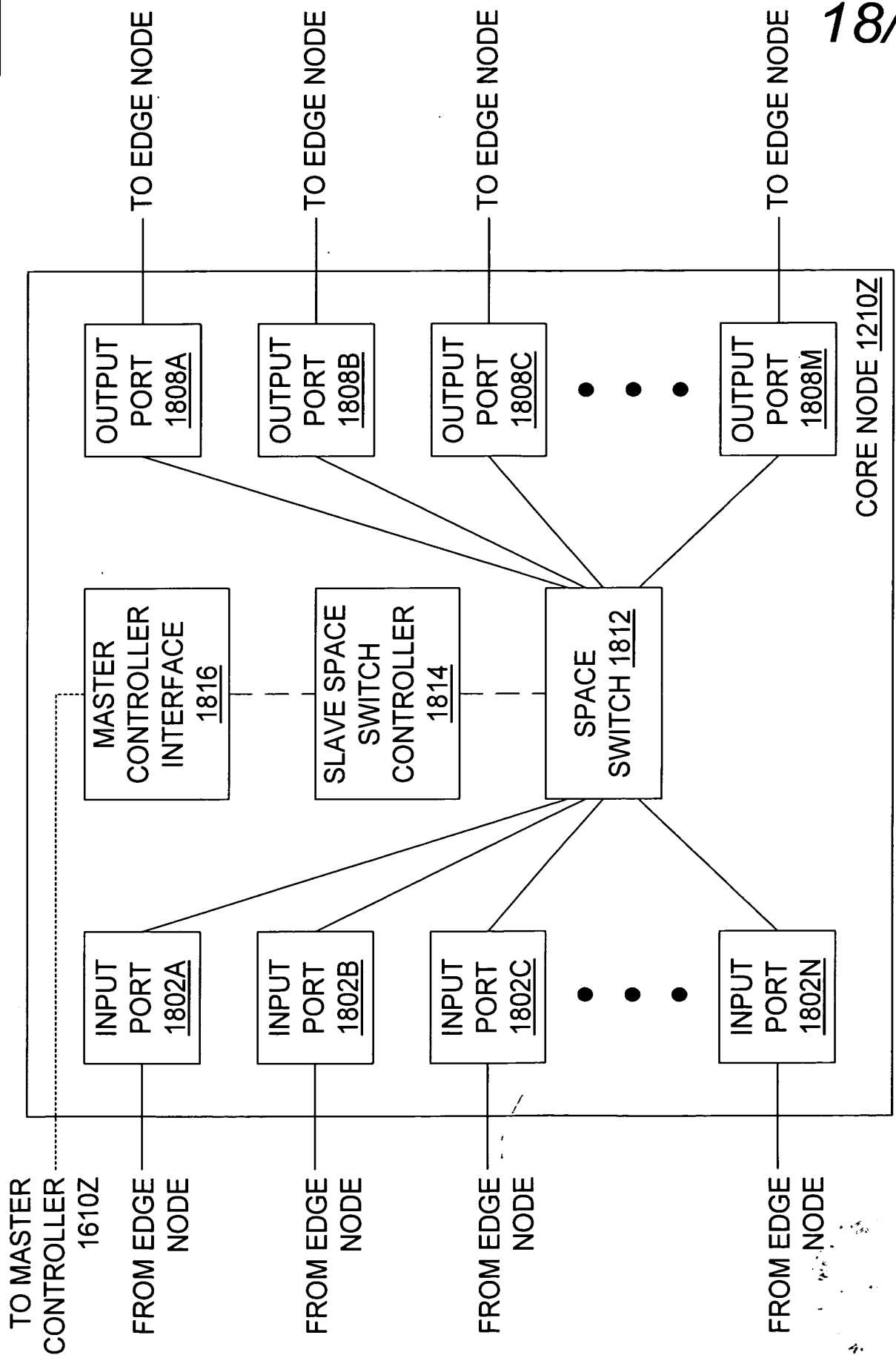


FIG. 18

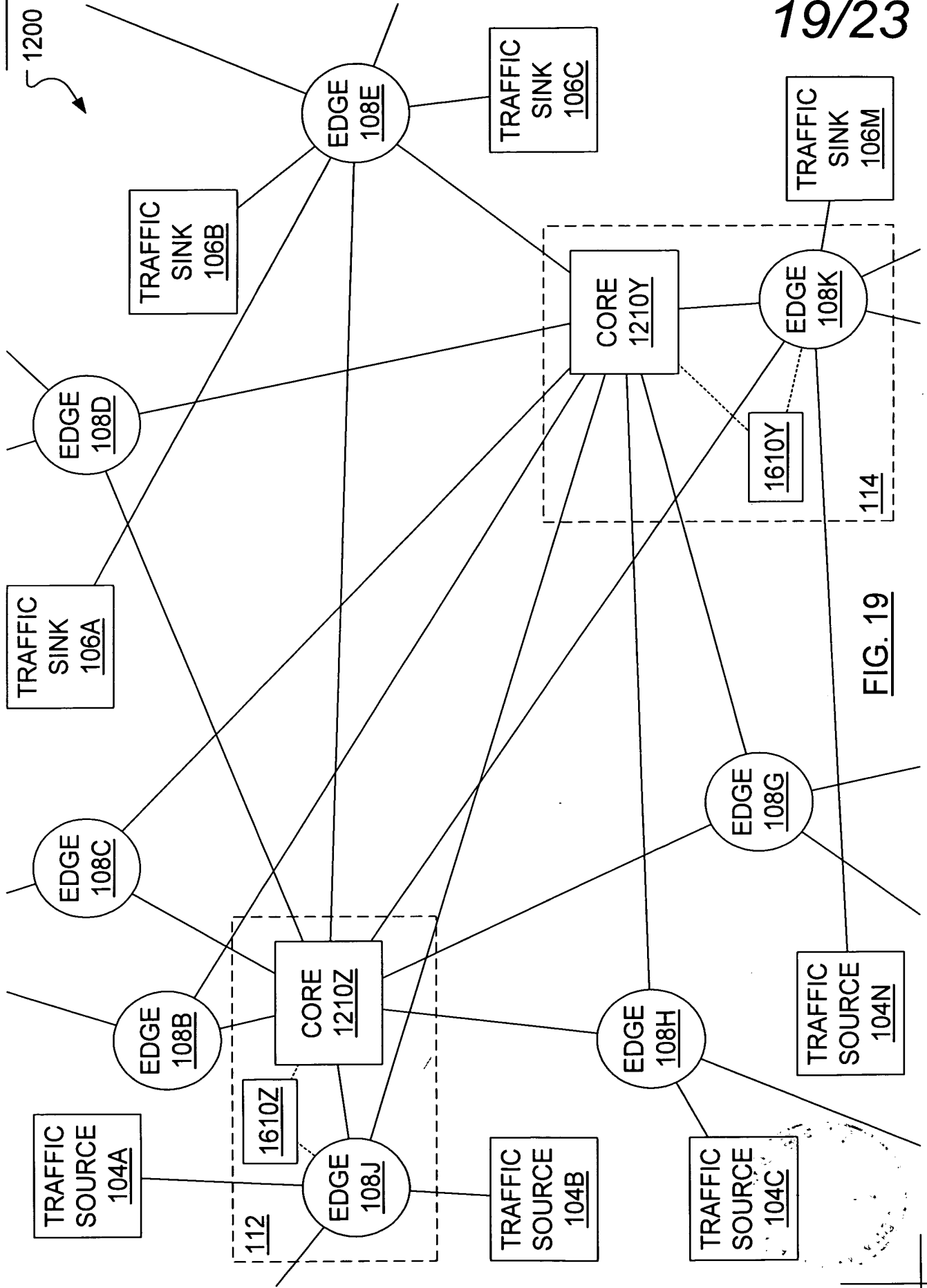
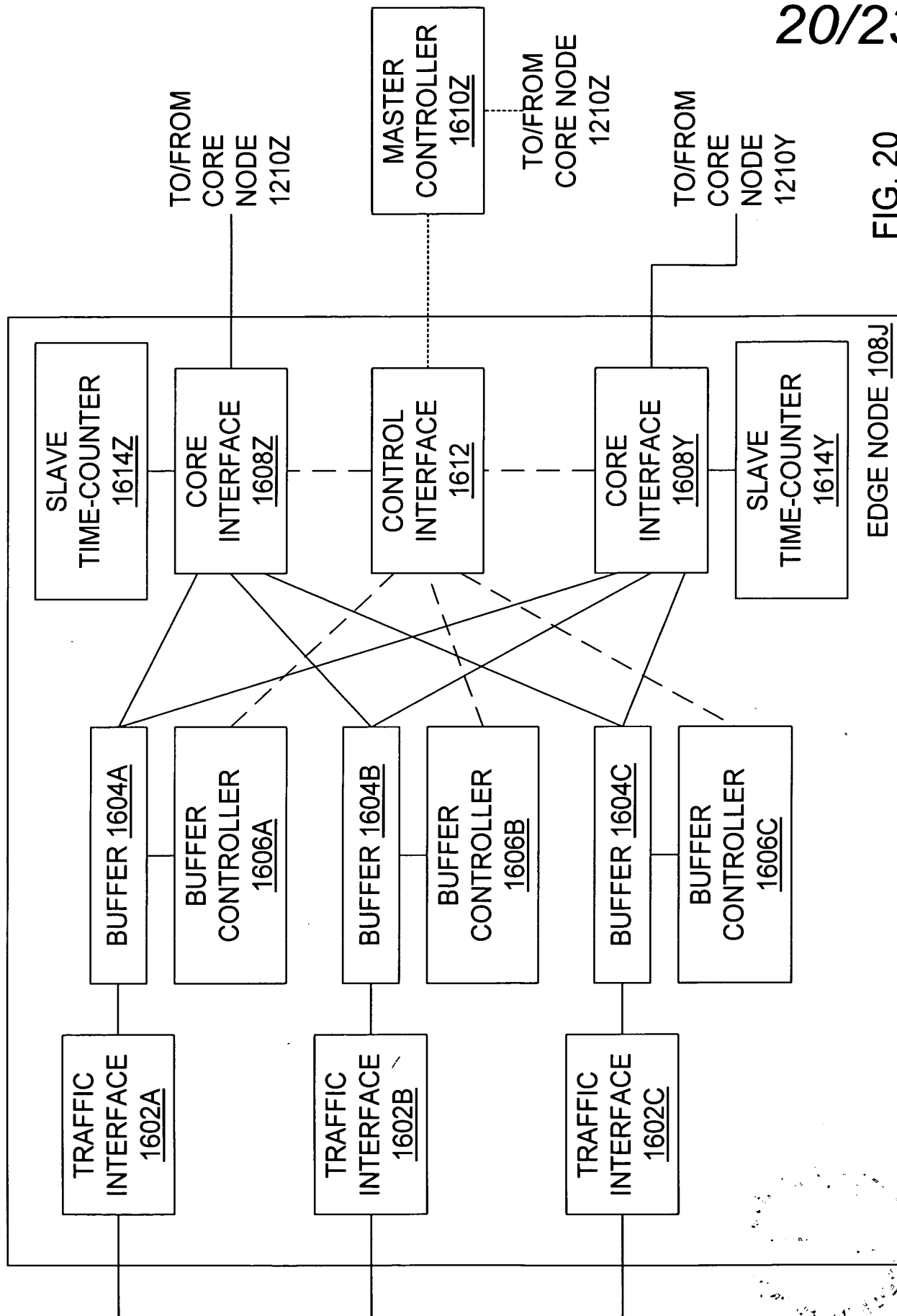


FIG. 19



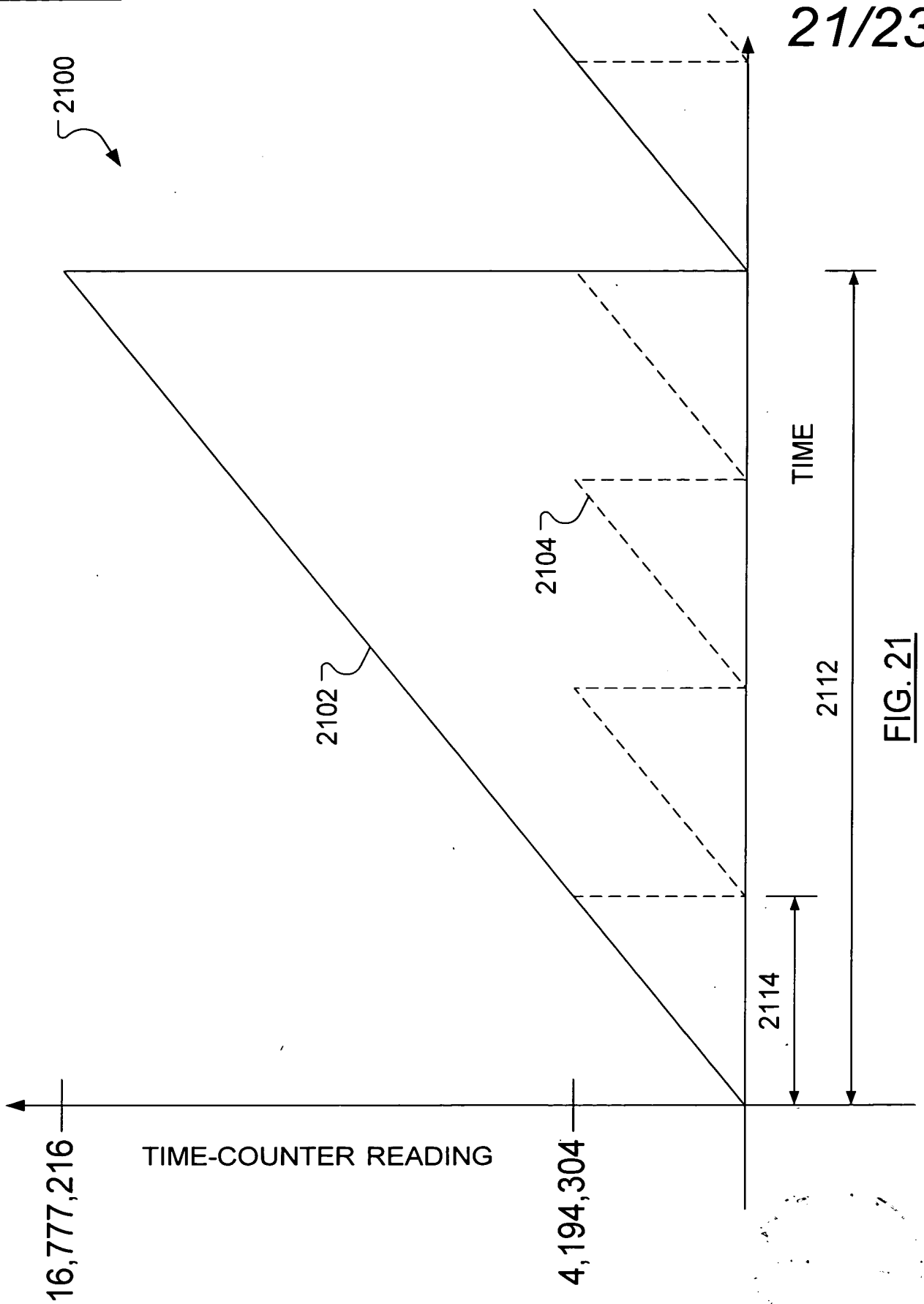


FIG. 21



FIG. 22

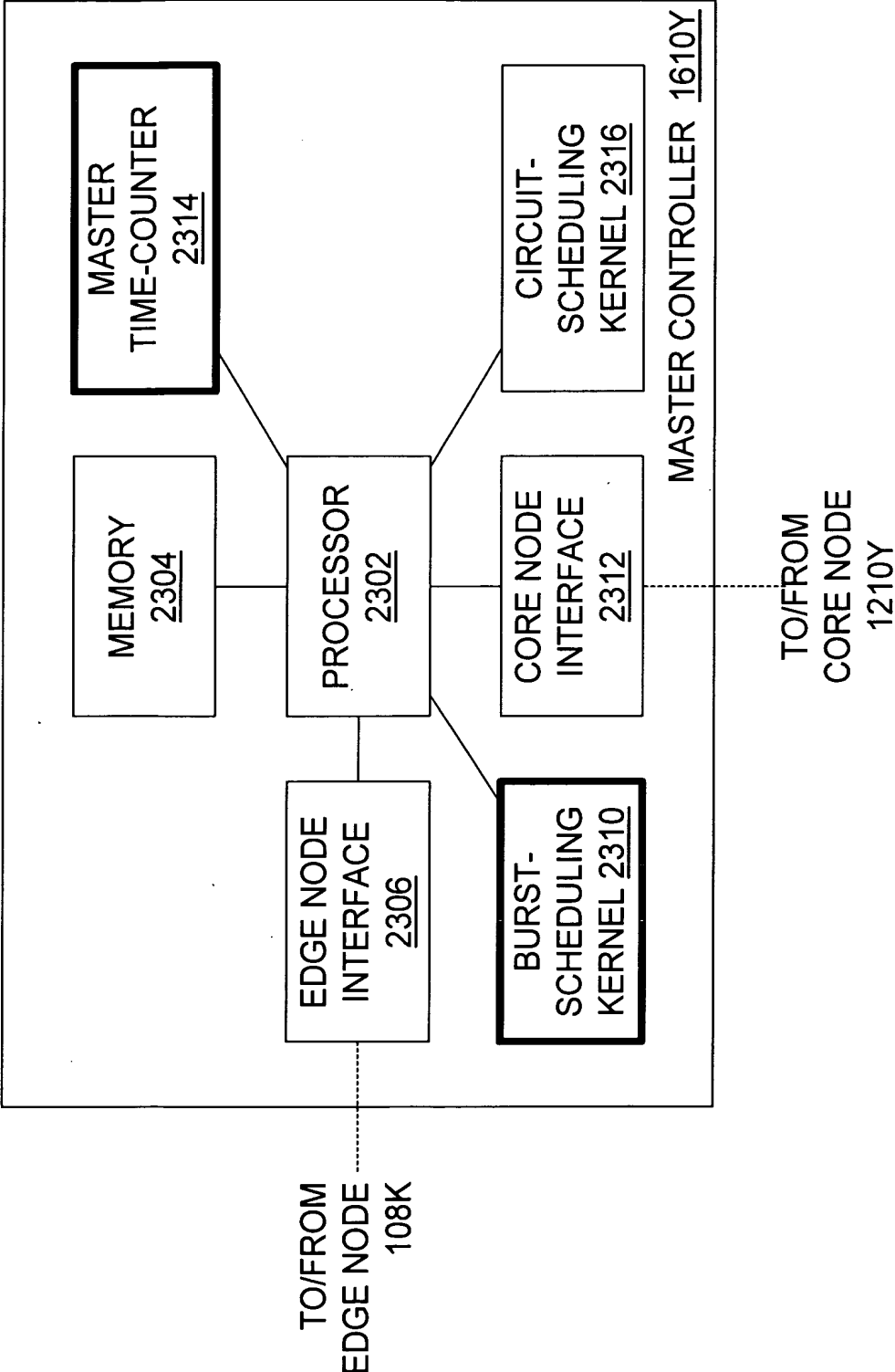


FIG. 23